

FIG. 1

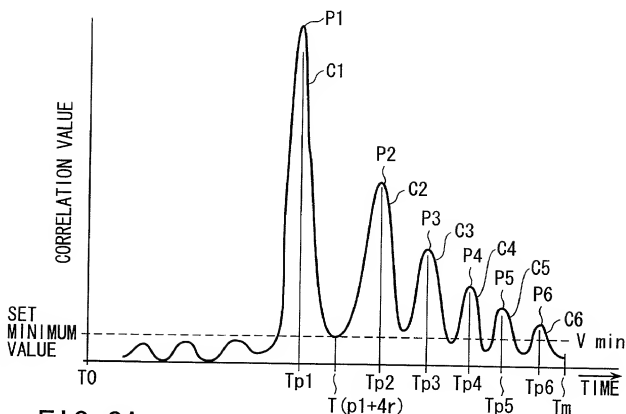


FIG. 2A

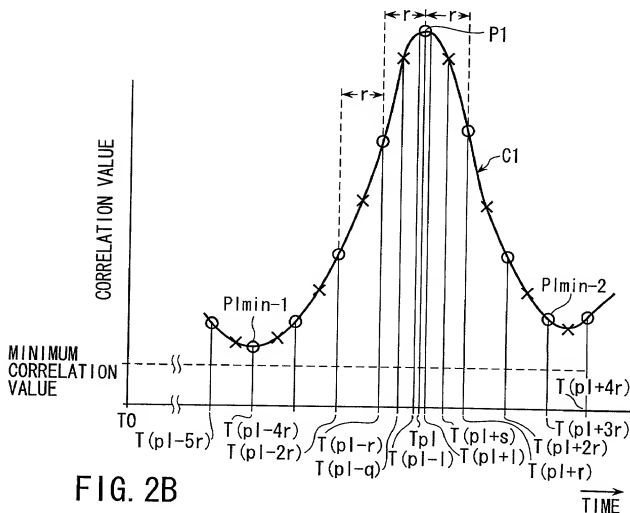


FIG. 2B

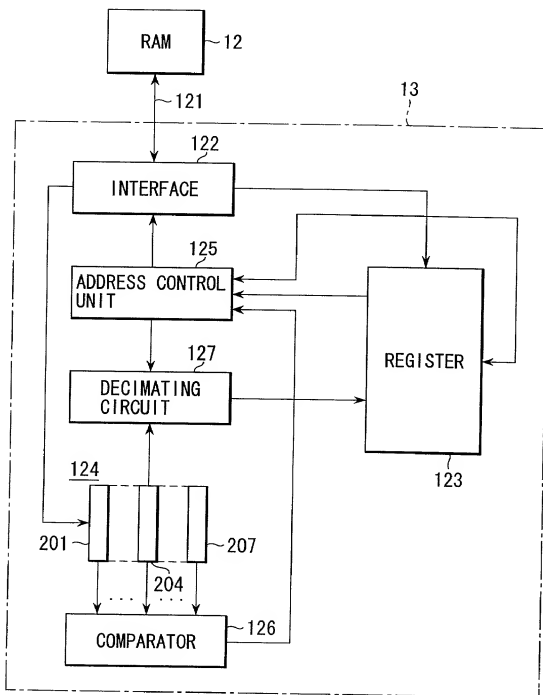


FIG. 3

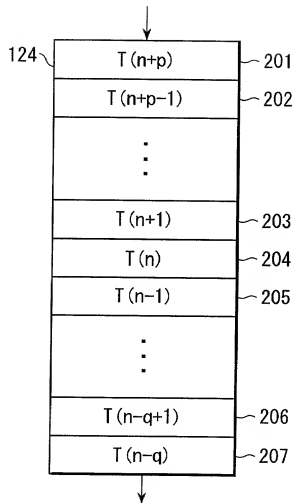


FIG. 4

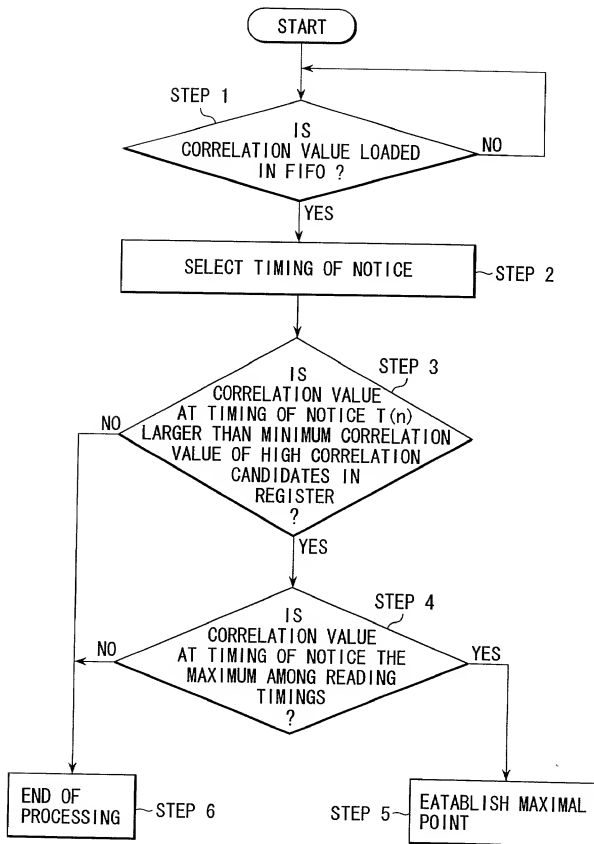


FIG. 5

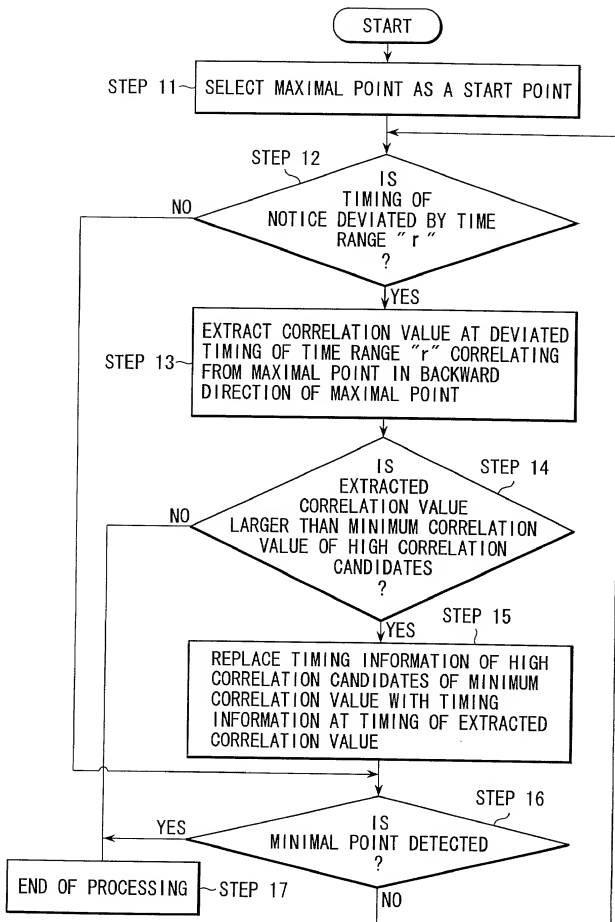


FIG. 6

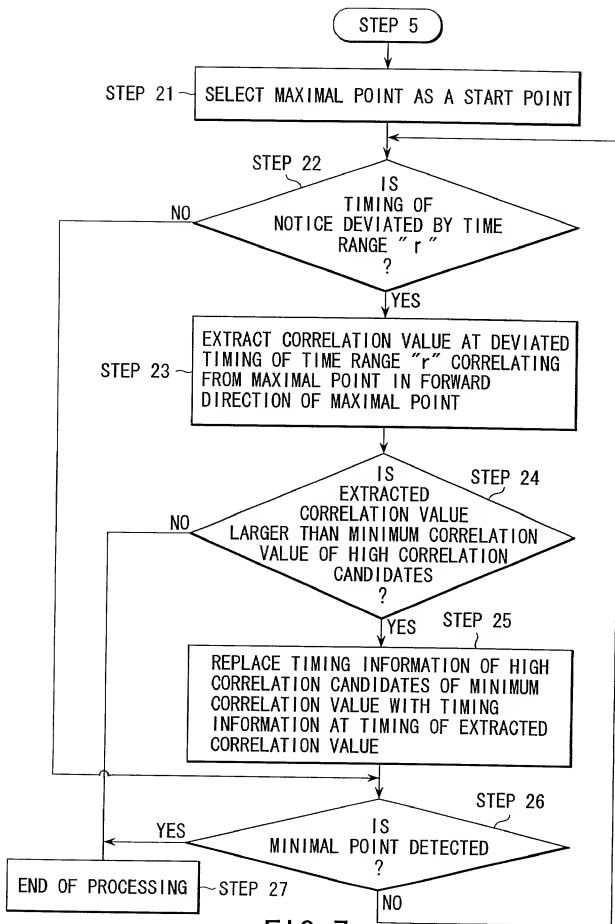
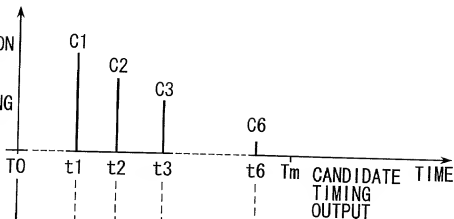


FIG. 7

OUTPUT CORRELATION  
VALUE OF CODE  
SYNCHRONOUS  
TIMING DETERMINING  
DEVICE

FIG. 8A



COR1 INPUT

COR2 INPUT

COR3 INPUT

COR6 INPUT

FIG. 8B

TIME

COR1 OUTPUT

COR2 OUTPUT

COR3 OUTPUT

COR6 OUTPUT

FIG. 8C

TIME

$T_0$

$T_m$